

Time-Table for Online Classes AY: 2020-21 M. Tech (VLSI) (I Year I Semester)

M. Tech - VLSI

DAY/HOUR	09:00 -10:00	10:00 - 10:10	10:10 -11:10	11:10 - 11:30	11:30 -12:30 (For Theory) 11:30 - 13:00*(For Lab)
Monday	CMOS	BREAK	HDL	BREAK	NANO FAB
Tuesday	VLSI		HDL		CMOS LAB
Wednesday	IPR		ENGLISH		HDL LAB
Thursday	IPR		NANO FAB		CMOS
Friday	ENGLISH		CMOS		VLSI
Saturday	Seminar / Mentoring		Seminar/ Mentoring		-

S. No.	Course Code	Name of the Subject	Name of the Faculty	Faculty ID
1.	GR20D5071	Digital System Design using HDL (HDL)	Mr M Shankara Rao	1529
2.	GR20D5072	Digital CMOS IC Design (CMOS)	Dr V Sreehari	1444
3.	GR20D5074	Nano Fabrication and Wafer Technology (NANO-FAB)	Dr P Asisa Kumar	1566
4.	GR20D5078	VLSI Technology and Design (VLSI)	Dr. D L Chaitanya	461
5.	GR20D5079	HDL Simulation Lab (HDL-LAB)	Mr M Shankara Rao	1529
6.	GR20D5080	Digital CMOS IC Design Lab (CMOS-LAB)	Dr Sreehari	1444
7.	GR20D5011	Research Methodology and IPR (IPR)	Dr Sudharsan Jayabalan	1564
8.	GR20D5152	English for Research Paper Writing (English)	Dr Sucharan Reddy	1588

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Mandatory Courses	Elective Courses
Digital System Design using HDL (HDL)	Nano Fabrication and Wafer Technology (NANO-FAB)
Digital CMOS IC Design (CMOS)	VLSI Technology and Design (VLSI)